What Is Claimed Is:

1. A dual panel-type organic electroluminescent display device, comprising:

a first substrate and a second substrate bonded together to include a plurality of sub-pixel regions;

a first electrode on an inner surface of the second substrate;

an insulating pattern on the first electrode along a border portion between adjacent sub-pixel regions;

a plurality of partition walls on the insulating pattern;

a plurality of organic electroluminescent layers, each within one of the sub-pixel regions between adjacent partition walls;

a second electrode on the organic electroluminescent layer;

a plurality of thin film transistors on an inner surface of the first substrate each within one of the sub-pixel regions, and including a semiconductor layer, a gate electrode, a source electrode, and a drain electrode;

a passivation layer covering the thin film transistors and including a contact hole exposing the drain electrode; and

a plurality of connection patterns on the passivation layer, each including a first pattern and a second pattern,

wherein the first pattern corresponds to the second electrode and has a height larger than a height of the partition walls and the second pattern covers the first pattern and is connected to the drain electrode and the second electrode.

2. The device according to claim 1, wherein light produced by the organic electro-luminescent layers is emitted through the first electrode.

3. The device according to claim 1, wherein each of the partition walls have a trapezoidal shape including a first side and a second side parallel to each other such that the first side contacts the insulating pattern and the second side is wider than the first side.

4. The device according to claim 1, wherein the first pattern includes photoresist material and is formed through photolithographic processes.

5. The device according to claim 1, wherein the first pattern includes at least one of photo-acryl and polyimide.

6. The device according to claim 1, further comprising a seal pattern along a peripheral portion between the first and second substrates.

7. The device according to claim 1, wherein each of the thin film transistors are driving thin film transistors providing current to an organic electroluminescent diode that includes the first electrode, the second electrode, and one of the organic electroluminescent layers.

8. The device according to claim 7, further comprising a switching thin film transistor including a drain electrode, wherein the gate electrode of the driving thin film transistor is connected to the drain electrode of the switching thin film transistor.

9. The device according to claim 1, wherein the first pattern has tapered sides.

10. The device according to claim 1, wherein the first pattern uniformly maintains a space between the first and second substrates.

11. A dual panel-type organic electroluminescent display device, comprising:

a first substrate and a second substrate bonded together having a plurality of sub-pixel regions;

a first electrode on an inner surface of the second substrate;

an insulating pattern on the first electrode along a border portion between adjacent sub-pixel regions;

a plurality of partition walls on the insulating pattern;

a plurality of organic electroluminescent layers, each at one of sub-pixel regions between adjacent partition walls;

a second electrode on the organic electroluminescent layer;

a semiconductor layer on an inner surface of the first substrate in the sub-pixel regions, and including an active region, a source region, and a drain region;

a gate insulating layer on the active region of the semiconductor layer;

a gate electrode on the gate insulating layer;

a passivation layer covering the gate electrode and including a first contact hole exposing a portion of the source region and a second contact hole exposing a portion of the drain region;

a plurality of first patterns on the passivation layer, each of the first patterns corresponding to the second electrode and having a height greater than a height of the partition walls;

a source electrode on the passivation layer and connected to the source region through the first contact hole;

a drain electrode on the passivation layer and connected to the drain region through the second contact hole; and

a second pattern covering the first pattern, the second pattern contacting the drain electrode and the second electrode,

wherein the semiconductor layer, the gate electrode, the source electrode, and the drain electrode constitute a thin film transistor, and the first pattern and the second pattern constitute a connection pattern.

12. The device according to claim 11, wherein light from each of the organic electro-luminescent layers is emitted through the first electrode.

13. The device according to claim 11, wherein each of the partition walls have a trapezoidal shape including a first side and a second side parallel to each other such that the first side contacts the insulating pattern and the second side is wider than the first side.

14. The device according to claim 11, wherein the first pattern includes photoresist material and is formed through photolithographic processes.

15. The device according to claim 11, wherein the first pattern includes at least one of photo-acryl and polyimide.

16. The device according to claim 11, further comprising a seal pattern along a peripheral portion between the first and second substrates.

17. The device according to claim 11, wherein each of the thin film transistors are driving thin film transistors providing current to an organic electroluminescent diode that includes the first electrode, the second electrode, and one of the organic electroluminescent layers.

18. The device according to claim 17, further comprising a plurality of switching thin film transistors each including a drain electrode, wherein each of the gate electrodes of the driving thin film transistors are connected to one of the drain electrodes of the switching thin film transistors.

19. The device according to claim 11, wherein the first pattern has tapered sides.

20. The device according to claim 11, wherein the first pattern uniformly maintains a space between the first and second substrates.